

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

CHOUBAL et al.

Serial No.: 10/805,105

Filed: March 19, 2004

For: MANAGING INPUT/OUTPUT  
(I/O) REQUESTS IN A CACHE MEMORY  
SYSTEM

Examiner: Stephen C. Elmore

Art Unit: 2185

**REQUEST FOR STATUS**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sirs:

This application was filed March 19, 2004, and a Notice of Publication was received on September 30, 2005. No subsequent papers have been received in this case.

Please send the undersigned a Status Report on this application.

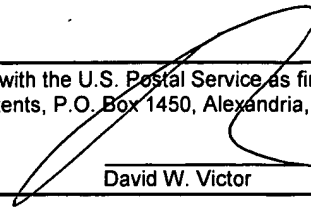
Respectfully submitted,

  
David W. Victor  
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Dated: May 10, 2006

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail on the date indicated above and is addressed to : Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
David W. Victor5/10/06  
Date